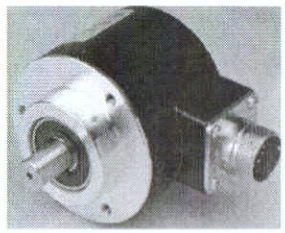
ASC6210/5810 Series Absolute Rotary Encoder



Features:

- Natural binery output.
- Superior for dampproof, anti-shock and anti-crash, high anti-interferance.
- Zero point fixed, non-influced by power OFF or system OFF, output uniform function.
- Reliable performace, compact in size and light in weight.
- Application: to measure angle or speed applied for automation measuring or Automation system control.

Model Number:

ASC6210/5810 - 0010 - G B - 5 E -S M -S

- ASC6210/5810: Series
- 0010: No. Of Bits
- G: Cable; C: Connector
- B: Binary; G: Grey code
- 5: Power 5VDC; Power 12VDC; Power 24VDC
- E: Voltage; L: TTL; C: NPN open Collector
- -S: S:SSI output
- M: M: Positive Logic; N: Negative Logic
- -S: S:Series connect; P:Parallel connect

Specification:

Model: ASC	Absolute Code						
	Bits	PPR	Resolution	Output Code	Direction Of Bits	Accuracy	
ASC6210/5810G-0013	13	8192	$360°/2^{13}$	Natural Binary	Axle head CW	±1.5'	
ASC6210/5810G-0012	12	4096	$360°/2^{12}$	Natural Binary	Axle head CW	±3'	
ASC6210/5810G-0011	11	2048	$360°/2^{11}$	Natural Binary	Axle head CW	±6'	
ASC6210/5810G-0010	10	1024	$360°/2^{10}$	Natural Binary	Axle head CW	±12'	

Electrical Features:

	Power (V)	Output Voltage			Consumption	Output Wave		Up-Down Time	Response Speed	Insulation Strength
		High (V)	Low (V)							
Absolute Code	5	>2.5	<1	mA	<100mA			<μs	0-1MS	>100MΩ

Electrical Wiring:

Wire Color	Red	Black	Green	White	Yellow	Brown	Grey	Orange	Shield
Signal (Voltage 5V)	Vcc	0V	Clock	Date	Reserve	—	—	Reserve	Shell
Signal (TTL)	Vcc	0V	Clock	Date	Reserve	Clock-	Date-	Reserve	Shell

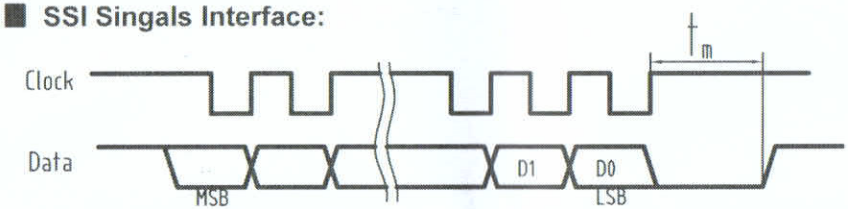
Mechanical Features:

Tacho Speed	Start Torque (25°C)	Shaft Load	
		Axial	Radial
0-6000r/min	$2X10^{-3}$N.m	10 N	20 N

Ambient features:

Usage Temp.	Storage Temp.	Humidity RH	Anti-Shock	Anti-Crash
-25~100°C	-30~110°C	30-85%	$30m/s^2$ (10~200Hz, x、y、z 3 directions 1h)	$300m/s^2$ (11ms, x、y、z 3 directions 3 times

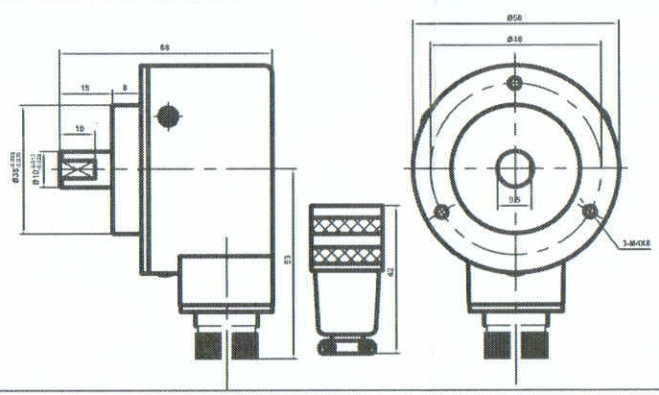
SSI Singals Interface:



Clock≤1MKz 20us≤ t_m ≤40us

1> The start position of absolute encoder is triggerred by the user end pulse or signals.

2> Encoder clock end accept signal from user end, Date end output the current bits from high bits(MSB), then Convert to Series SSI signals code (international standard SSI protocol).

3> When without signals, Clock end and Date End are both on high bits, start to save data until the first Trailing edg of Clock end, and start to transmit data until the rising edg of clock end.

Encoder Dimension: